Art Unit No.: 2825

## **REMARKS**

Claims 1-11 are pending in this application.

Claim 6 has been amended into an independent claim incorporating all of the limitations of the preceding claims previously depended from, thus obviating the examiner's objection.

Claim 8 has been amended to remove the Examiner's objection.

## Claim Rejections

The Office Action improperly rejected Claims 1-5 and 7-11 as being obvious over Gowni et al. in view of Chu et al.

The Office acknowledges that Gowni *et al.* do not teach an integrated circuit design including a power management unit controlling a supply voltage. The Office improperly attempts to use Chun to correct this deficiency.

The present claims are directed to a method of creating a design for a semiconductor memory. Gowni *et al.* only discloses a method for automated design of memory devices. Gowni does not disclose providing a user selectable option to selectively allow enablement of an ultra low power feature as part of the design and does not disclose providing a power management circuit design.

The Office contends that Chu teaches an integrated circuit design that includes a power management unit to control a supply voltage within a data processing system, where the data processing system includes a plurality of processors that utilize different supply voltages.

The only teachings from Chu is that if the processors have different supply voltages, a voltage control module can be used to provide the different supply voltages to

U.S. Patent Application Serial No.: 10/752,116

Art Unit No.: 2825

each of the plurality of processors. There is no showing in the present claims of a plurality of processors with a plurality of different voltages required.

Furthermore, Chu does not teach including an ultra low power feature that is selectably the user, or a power management design that is selected by the user, rather teaches only a voltage module that can supply variable voltages.

Chu and Gowni cannot be combined to arrive at the present claims.

The rejection is improper and must be withdrawn.

## **Conclusion**

The disclosure of Chu, while disclosing a voltage module that can be used as a power management circuit, does not provide a teaching for having a user selectable option for a ultra lower power feature. Thus, the rejection has been shown to be improper.

The Examiner is invited to contact the undersigned by telephone if it is felt that a telephonic or in-person interview would advance the prosecution of the Application.

U.S. Patent Application Serial No.: 10/752,116

Art Unit No.: 2825

Although a fee is not deemed necessary, the Office is hereby requested and authorized to charge any fee required to enter the instant amendment against Deposit Account No. 04-1679 to Duane Morris LLP.

Respectfully Submitted

Patrick C. Muldoon

Reg. No. 47,343

Attorneys at Law

DUANE MORRIS LLP 1667 K Street, N.W., Suite 700 Washington, DC 20006 Telephone: (202) 776-7800

Telecopier: (202) 776-7801

Dated: January 24, 2006